

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2015-16
II B.Tech I semester ECE Section-A

Room No : 203 North Block (NB)

w.e.f: 27-07-2015

Day	930-1030	1030-1130	1130 - 1230	1230 – 1330	1330 - 1430	1430-1530	1530-1630
Monday	SS	EDC(G1-203) SS(G2-301) EC(G3-302)	EC	LUNCH	EDC/BS LAB		
Tuesday	EC	STLD(G1-203) PTSP(G2-301) M-III(G3-302)	PTSP		SS	M -III	STLD
Wednesday	M -III	EDC	STLD		PTSP(G1-203) M-III(G2-301) STLD(G3-302)	PTSP	EC
Thursday	BS/EDC LAB				EDC	SS(G1-203) EC(G2-301) EDC(G3-302)	SS
Friday	EDC	M -III	SS		STLD	M-III(G1-203) STLD(G2-301) PTSP(G3-302)	PTSP
Saturday	PTSP	STLD	EC(G1-203) EDC(G2-301) SS(G3-302)		M -III	EC	EDC

S.No	Subject		Faculty	Dept
1	M III	Mathematics -III	G.Sangeetha	Maths
2	PTSP	Probability Theory & Stochastic Processes	B.Kiran mai	ECE
3	STLD	Switching Theory & Logic design	B.Jyothsna	ECE
4	EC	Electrical Circuits	T.Vinay Kumar	EEE
5	EDC	Electronic devices & circuits	S.Suresh	ECE
6	SS	Signals & Systems	D.Ramesh	ECE
7	EDC LAB	Electronic devices & circuits Lab	S.Suresh,Asma Begum	ECE
8	BS LAB	Basic Simulation Lab	D.Ramesh, A.Jyothirmai/C.Srinivas	ECE

Tutorial Groups :

G1: 401-424

G2: 425-449

G3: 450-Last Number

Incharge
B.Prabhavathi/S.Deepika

HOD
Dr.K.Yedukondalu

Principal
Dr.G.Ravinder Reddy